IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Shunpei Yamazaki, et al.

Art Unit : 1792

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Title : SI

: June 25, 2001 Conf. No. : 1969 : SEMICONDUCTOR DEVICE AND FABRICATION METHOD THEREFOR

MAIL STOP RCE

Commissioner for Patents

P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Applicants request consideration of the references listed on the attached PTO-1449 form.

Copies of any listed U.S. patents or U.S. patent application publications can be provided upon request.

This filing is being made with the filing of a Request for Continued Examination. No fee is required.

Respectfully submitted

Date: October 30, 2007

Roberto J. Devoto Reg. No. 55,108

Fish & Richardson P.C. 1425 K Street, N.W. 11th Floor Washington, DC 20005-3500 Telephone: (202) 783-5070 Facsimile: (202) 783-2331

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